



<p style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i></p>				Complete If Known													
Sheet 1 of 2		<table border="1"> <tr> <td>Application Number</td> <td>10/035,574</td> </tr> <tr> <td>Filing Date</td> <td>October 18, 2001</td> </tr> <tr> <td>First Named Inventor</td> <td>Mark Anders</td> </tr> <tr> <td>Art Unit</td> <td>2611</td> </tr> <tr> <td>Examiner Name</td> <td>Sudhanshu C. Pathak</td> </tr> <tr> <td>Attorney Docket Number</td> <td>42390P11426</td> </tr> </table>				Application Number	10/035,574	Filing Date	October 18, 2001	First Named Inventor	Mark Anders	Art Unit	2611	Examiner Name	Sudhanshu C. Pathak	Attorney Docket Number	42390P11426
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Art Unit	2611																
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Attorney Docket Number	42390P11426																

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
SP		US-6,005,417	12-21-1999	MEHTA et al.	
		US-6,028,814	02-22-2000	LIM	
		US-6,140,843	10-31-2000	HOWARD	
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		US-6,417,698	07-09-2002	WILLIAMS et al.	
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		US-2003/0107411	06-12-2003	MARTIN et al.	
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SP		US-2006/0140034	06-29-2006	HSU et al.	
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Signature	/Sudhanshu Pathak/	Date Considered	08/04/2006
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet

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of

2

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Application Number	10/035,574
Filing Date	October 18, 2001
First Named Inventor	Mark Anders
Art Unit	2611
Examiner Name	Sudhanshu C. Pathak
Attorney Docket Number	42390P11426

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SP		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", Symposium on VLSI Circuits, Digest of Technical Papers, pp. 16-17 (Jun. 13-15, 2002).	
SP		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", IEEE Journal of Solid-State Circuits, Vol. 38, No. 5, pp. 709-714 (May 2003).	
SP		Stan, Mircea R., et al., "Bus-Invert Coding for Low-Power I/O", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 3, No. 1, pp. 49-58 (Mar. 1995).	

Examiner Signature	/Sudhanshu Pathak/	Date Considered	08/04/2006
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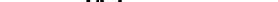


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Information Disclosure Statement by Applicant				Complete if Known
Sheet	1	of	1	
				Application Number 10/035574
				Filing Date October 18, 2001
				First Named Inventor Mark Anders
				Art Unit 2634
				Examiner Name Sudhanshu C. Pathak
				Attorney Docket Number 42390P11426

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	10	HSU et al - US Patent Application No. 11/025,778 entitled TRANSITION-ENCODER SENSE AMPLIFIER filed 12/29/2004

Examiner Signature		Date Considered	08/21/06
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